

JCE992 U.S. PRO
09/667559
09/22/00

		Subclass	ISSUE CLASSIFICATION
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PATENT NUMBER

TC-2
TC-859

U.S. UTILITY Patent Application

O.I.P.E.
KU 113 Q.A. 118
SCANNED

PATENT DATE

APPLICATION NO. 09/667559	CONT/PRIOR F	CLASS 257	SUBCLASS 500+	ART UNIT 28114	EXAMINER FARAHANI CHANDRUPATI
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Power MOS transistor having trench gate

PTO-2040
12/89

ISSUING CLASSIFICATION

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____				ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	

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